L	Hits	Search Text	DB	Time stamp
Number -	2	semiconductor ADJ device with wait ADJ	USPAT	2004/05/24
	_	time		16:02
-	37	semiconductor with wait ADJ time	USPAT	2004/05/24 16:02
-	109	semiconductor with wait\$3 ADJ time	USPAT	2004/05/24
-	109	semiconductor with wait\$3 ADJ time	USPAT	2004/05/24
-	593	semiconductor ADJ device AND wait\$3 ADJ	USPAT	2004/05/24
-	6	time semiconductor ADJ device AND wait\$3 ADJ	USPAT	16:04 2004/05/24
-	О	time with elapse 714/815.ccls. and semiconductor ADJ	USPAT	16:07 2004/05/24
_	1	device AND wait\$3 ADJ time with elapse 714/815.ccls. and semiconductor ADJ	USPAT	16:08 2004/05/24
-	196	device AND wait\$3 ADJ time 714/815.ccls.	USPAT	16:08 2004/05/24
-	40	714/815.ccls. and (wait or delay) adj	USPAT	16:09 2004/05/24
- `	5	tim\$3 714/815.ccls. and (wait or delay) adj	USPAT	16:35 2004/05/24
_	1	tim\$3 with measur\$5 714/815.ccls. and (wait or idle) adj	USPAT	16:37 2004/05/24
		tim\$3 with measur\$5		16:37
-	6	714/815.ccls. and wait adj tim\$3	USPAT	2004/05/24 16:41
-	287	714/\$.ccls. and wait adj tim\$3	USPAT	2004/05/24 16:41
-	287	714/\$.ccls. and wait adj tim\$3	USPAT	2004/05/24
-	3	714/\$.ccls. and wait adj tim\$3 with measur\$5	USPAT	2004/05/24
-	27		USPAT	2004/05/24
	0	714/\$.ccls. and taet adj pattern and wait adj tim\$3	USPAT	2004/05/24
-	19	714/\$.ccls. and test adj pattern and wait	USPAT	2004/05/24
7	188	1 3 .	USPAT	2004/05/24
-	25	714/\$.ccls. and test adj pattern and wait\$ with tim\$3 and device adj under	USPAT	2004/05/24 17:50
-	160	adj test 714/\$.ccls. and test adj pattern and (wait\$ or delay) with tim\$3 and device adj under adj test	USPAT	2004/05/24 18:11
-	31	714/\$.ccls. and test adj pattern and (wait\$ or delay) with tim\$3 with compar\$4 and device adj under adj test	USPAT	2004/05/24 18:10
~	0	714/\$.ccls. and test adj pattern and variable ADJ time ADJ delay and device adj under adj test	USPAT	2004/05/24 18:12
~	1	714/\$.ccls. and test adj pattern and variable ADJ time ADJ delay	USPAT	2004/05/24
-	0	714/\$.ccls. and test adj pattern and variable ADJ wait\$3 ADJ time	USPAT	2004/05/24
-	0	714/\$.ccls. and test adj pattern and adjustable with wait\$3 ADJ time	USPAT	2004/05/24 18:13
_	0	714/\$.ccls. and test adj pattern and adjust\$3 with wait\$3 adj (tim\$3 or period)	USPAT	2004/05/24 18:14
-	6	714/\$.ccls. and test adj pattern with wait\$3 adj (tim\$3 or period)	USPAT	2004/05/24
-	17	714/\$.ccls. and test adj pattern with	USPAT	18:16 2004/05/24
~	0	wait\$3 with (tim\$3 or period) "20020100000"	USPAT	18:28 2004/05/24
-	1	"20020100000"	USPAT;	18:28 2004/05/24
	l. <u></u>		US-PGPUB	18:28

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-	383	TOSHIBA.as. and wait\$3 adj tim\$3	USPAT	2004/05/24
				18:43
-	28175	TOSHIBA.as. and wait\$3 adj tim\$3 and test	USPAT	2004/05/24
		adj pattern and deviceADJ under adj test		18:42
-	0	I I I I I I I I I I I I I I I I I I I	USPAT	2004/05/24
		adj pattern and device ADJ under adj test		18:43
-	1	TOSHIBA.as. and wait\$3 adj tim\$3 and	USPAT	2004/05/24
		device ADJ under adj test		18:43
-	0	TOSHIBA.as. and wait\$3 adj tim\$3 with	USPAT	2004/05/24
- 11		semiconductor adj test\$3		18:44
-	0	TOSHIBA.as. and wait\$3 adj tim\$3 and	USPAT	2004/05/24
		semiconductor adj test\$3		18:46
-	1	JP08247817	USPAT;	2004/05/25
			EPO; JPO;	07:50
			DERWENT	
-	1	JP08247817	USPAT;	2004/05/25
			EPO; JPO;	07:50
			DERWENT	
_	1	measurement with adjust\$4 with operating	USPAT	2004/05/25
		with speed and TESTING with		07:52
		SEMICONDUCTOR near DEVICE		
]
-	1	measurement with adjust\$4 with speed and	USPAT	2004/05/25
		TESTING with SEMICONDUCTOR near DEVICE		07:53
			4.1	
_	19	adjust\$4 with speed and TESTING with	USPAT	2004/05/25
		SEMICONDUCTOR near DEVICE		07:57
				6
- 11	36	adjust\$4 with time with delay and TESTING	USPAT	2004/05/25
		with SEMICONDUCTOR near DEVICE		07:57
-	34	714/815.ccls. and (wait or delay) adj	USPAT	2004/05/25
		time		15:59